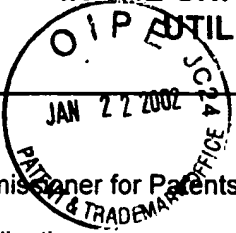


**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
UTILITY PATENT APPLICATION TRANSMITTAL
UNDER 37 CFR 1.53(b)**



Address to: Assistant Commissioner for Patents Box Patent Application Washington, DC 20231	Attorney Docket No.	2207/353102
	Inventor(s)	V. TYMCHENKO
	Express Mail Label No.	EL566655824US
	Total Pages	46

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Title of Application:

**APPARATUS, METHOD AND COMPUTER SYSTEM FOR REDUCING POWER
CONSUMPTION OF A PROCESSOR OR PROCESSORS UPON OCCURRENCE
OF A FAILURE CONDITION AFFECTING THE PROCESSOR OR PROCESSORS**

Transmitted with the patent application are the following:

- 2 Page(s) Transmittal form (and one copy)
- 28 Page(s) Specification (19), claims (8), abstract (1)
- 7 Page(s) Informal drawing
- 3 Page(s) Declaration and Power of Attorney (signed copies as originally filed)
- Page(s) Statement Claiming Small Entity Status
- Page(s) Other

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This application is a Continuation of prior application Serial No. 08/672,870, filed June 28, 1996.

Fee calculation for non-small entity:	No. Filed		No. Extra	Rate	Fee
Basic Fee					\$710.00
Total Claims	30	- 20 =	10	× 18.00	\$180.00
Independent Claims	3	- 3 =	0	× 80.00	\$0.00
Multiple Dependent Claim				270.00	
					Assignment
					Total
					\$890.00

The Commissioner is hereby authorized to charge payment of any fees associated with this communication or arising during the pendency of this application per 37 CFR §1.16-1.21, or to credit any overpayments, to Deposit Account 11-0600.

Express Mail Certificate

I hereby certify that the above paper/fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated below and is addressed to the Assistant Commissioner for Patents, Washington, DC 20231

Date of deposit: September 19, 2001

Person mailing paper/fee: Pilar Rodriguez

Signature: *Pilar Rodriguez*

Respectfully submitted,

Shawn W. O'Dowd
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Docket No. 2207 / 353102	Serial No.	Date of Mailing September 10, 2001
By SWO/PMR/SJO	Filing Date 09/10/01	Express Mail No. EL566655824US
Inventors/ Applicant V. TYMCHENKO		
Title APPARATUS, METHOD AND COMPUTER SYSTEM FOR REDUCING POWER CONSUMPTION OF A PROCESSOR OR PROCESSORS UPON OCCURRENCE OF A FAILURE CONDITION AFFECTING THE PROCESSOR OR PROCESSORS		

Item No.	Description	No. of Pages
1.	CONTINUATION APPLICATION TRANSMITTAL (plus 1 copy)	2
2.	PATENT APPLICATION	28
3.	INFORMAL DRAWINGS	7
4.	DECLARATION/POWER OF ATTORNEY (signed copy as originally filed)	3
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Docket No. 2207 / 353102	Serial No. 271 U.S. PTO 09/955007	Date of Mailing September 10, 2001
By SWO/PMR/SJO	Filing Date 09/10/01	Express Mail No. EL566655824US
Inventors/ Applicant V. TYMCHENKO		
Title APPARATUS, METHOD AND COMPUTER SYSTEM FOR REDUCING POWER CONSUMPTION OF A PROCESSOR OR PROCESSORS UPON OCCURRENCE OF A FAILURE CONDITION AFFECTING THE PROCESSOR OR PROCESSORS		

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